


PATENT NUMBER

257	404	Subclass
ISSUE CLASSIFICATION		

## U.S. UTILITY Patent Application

O.I.P.E.		PATENT DATE
SCANNED	LMU	Q.A. 10/24

APPLICATION NO. 09/890120	CONT/PRIOR D F	CLASS 257	SUBCLASS 34	ART UNIT -2811	EXAMINER 
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## APPLICANTS

Jean-Luc Felloie

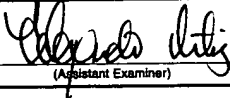
**TITLE**

Mos transistor with dynamic threshold voltage equipped with a current limiting device and method for making same

PTO-2040  
12/99

ISSUING CLASSIFICATION									
ORIGINAL		CROSS REFERENCE(S)							
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
257	404	257	345	347	376	412	413	424	
INTERNATIONAL CLASSIFICATION		438	149	282	289	217			
H101L	29/76								

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg. <div>8</div>	Figs. Drwg. <div>17</div>	Print Fig. <div>5</div>	Total Claims <div>11</div>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<div>  <div>1/13</div> <div>(Assistant Examiner) (Date)</div> </div>		<b>NOTICE OF ALLOWANCE MAILED</b>	
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____		<b>ISSUE FEE</b>	
Amount Due			Date Paid	
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	<div> <div>_____</div> <div>(Primary Examiner) (Date)</div> </div>		<b>ISSUE BATCH NUMBER</b>	
	<div> <div>_____</div> <div>(Legal Instruments Examiner) (Date)</div> </div>			
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